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Attorney Docket SEL 171

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:

Kadono et al.

Serial No.: 09/535,233

Filed: March 24, 2000

For: A Method Of Manufacturing A
Semiconductor Device

Art Unit: 2823

Examiner: W. Coleman

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

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Shannon Wallace

Name of applicant, assignee, or Registered Rep.

Shannon Wallace 8/17/04
Signature Date

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RESPONSE G (AFTER FINAL)

Sir:

Applicants have the following response to the Final Rejection dated May 17, 2004.

Entered
W/PCE
10/28/04
C. Davis

REMARKS

Claim Rejections - 35 USC §103

In the Final Rejection, the Examiner continues to reject Claims 11-18 under 35 USC §103 as being unpatentable over Lin et al. in view of Muraoka et al. The Examiner also continues to reject Claims 13, 14, 17 and 18 under 35 USC §103 as being unpatentable over Lin et al. in view of Muraoka et al. and further in view of Araujo. The Examiner also continues to reject Claims 19, 20, 23, 24, 27 and 28 under 35 USC §103 as being unpatentable over Lin et al. in view of Muraoka et al. and Yoshikawa. The Examiner also continues to reject Claims 21, 22, 25, 26, 29 and 30 under 35